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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application of:	Ngyuen, et al.	Confirmation No.:	9109
Serial No.:	10/662,204	Art Unit:	2819
Filed:	September 12, 2003	Examiner:	Cho, James Hyonchol
For:	<i>Adaptive Impedance Output Driver Circuit</i>	Attorney Docket No.:	60809-0127-US

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure provisions of 37 C.F.R. §1.56, there is hereby provided certain information which the Examiner may consider material to the examination of the subject U.S. patent application. It is requested that the Examiner make this information of record if it is deemed material to the examination of the application.

1. Enclosures accompanying this Information Disclosure Statement are:
  - 1a. ☒ A list of all patents, publications, applications, or other information submitted for consideration by the office.
  - 1b. A legible copy of :
    - ☒ Each U.S. patent application publication and U.S. and foreign patent;
    - ☒ Each publication or that portion which caused it to be listed on the PTO-1449;
    - ☐ For each cited pending U.S. application, the application specification including the claims, and any drawing of the application, or portion of the application which caused it to be listed on the PTO-1449 including any claims directed to that portion;
    - ☐ all other information or portion which caused it to be listed on the PTO-1449.
  - 1c. ☐ An English language copy of search report(s) from a counterpart foreign application or PCT International Search Report.
  - 1d. ☐ Explanations of relevancy (ATTACHMENT 1(d), hereto) or English language abstracts of the non-English language publications.
2. ☒ This Information Disclosure Statement is filed under 37 C.F.R. §1.97(b):
  - ☐ Within three months of the filing date of a national application other than a continued prosecution application under §1.53(d);

- ☐ Within three months of the date of entry of the national stage as set forth in §1.491 in an international application;
- ☐ Before the mailing of the first Office action on the merits;
- ☒ Before the mailing of a first Office action after the filing of a request for continued examination under §1.114.

3. ☐ This Information Disclosure Statement is filed under 37 C.F.R. §1.97(c) after the period specified in 37 C.F.R. §1.97(b), but before the mailing date of any of a final action under 37 C.F.R. §1.113, a notice of allowance under 37 C.F.R. §1.311 or an action that otherwise closes prosecution in the application.

*(Check either Item 3a or 3b)*

- 3a. ☐ The Certification Statement in Item 5 below is applicable. Accordingly, no fee is required.
- 3b. ☐ The \$180.00 fee set forth in 37 C.F.R. §1.17(p) in accordance with 37 C.F.R. §1.97(c) is:
- ☐ enclosed
  - ☐ to be charged to Morgan, Lewis & Bockius LLP Deposit Account No. 50-0310 (order no. \_\_\_\_\_).

*(Item 3b to be checked if any reference known for more than 3 months)*

4. ☐ This Information Disclosure Statement is filed under 37 C.F.R. §1.97(d) after the period specified in 37 C.F.R. §1.97(c), but on or before the date of payment of the issue fee.

*(Check either Item 4a or 4b)*

- 4a. ☐ The Certification Statement in Item 5 below is applicable.
- 4b. ☐ The \$180.00 fee set forth in 37 C.F.R. §1.17(p) is:
- ☐ enclosed.
  - ☐ to be charged to Morgan, Lewis & Bockius LLP Deposit Account No. 50-0310 (order no. \_\_\_\_\_).

5. ☐ Certification Statement (applicable if Item 3a or Item 4a is checked)

*(Check either Item 5a, 5b or 5c)*

- 5a. ☐ In accordance with 37 C.F.R. §1.97(e)(1), it is certified that each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement.
- 5b. ☐ Each item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart application, and the communication was not **received** by any individual designated in 37 C.F.R. §1.56(c) more than thirty days prior to the filing of this information disclosure statement.
- 5c. ☐ Pursuant to 37 C.F.R. §1.704(d), each item of information contained in this information disclosure statement was cited in a communication from a foreign

patent office in a counterpart application, and the communication was not **received** by any individual designated in 37 C.F.R. §1.56(c) more than thirty days prior to the filing of this information disclosure statement.

6. ☒ Copies of each cited U.S. patent and each U.S. patent application publication are not enclosed pursuant to the USPTO OG Notice dated 05 August 2003 waiving the requirement under 37 C.F.R. 1.98(a)(2)(i) for U.S. patent applications filed after June 30, 2003.

7. ☐ This application is a continuation application under 37 C.F.R. §1.53(b) or (d).

*(Check appropriate Items 7a, 7b and/or 7c)*

- 7a. ☐ A Petition to Withdraw from issue under 37 C.F.R. §1.313(b)(5) is concurrently filed herewith.

- 7b. ☐ Copies of publications listed on Form PTO-1449 from prior application Serial No. \_\_\_\_\_, filed on \_\_\_\_\_, of which this application claims priority under 35 U.S.C. §120, are not being submitted pursuant to 37 C.F.R. §1.98(d).

- 7c. ☐ Copies of the publications listed on Form PTO-1449 were not previously cited in prior application Serial No. \_\_\_\_\_, filed on \_\_\_\_\_, and are provided herewith.

8. ☐ This is a Supplemental Information Disclosure Statement. (Check Item 8a)

- 8a. ☐ This Supplemental Information Disclosure Statement under 37 C.F.R. §1.97(f) supplements the Information Disclosure Statement filed on \_\_\_\_\_. A bona fide attempt was made to comply with 37 C.F.R. §1.98, but inadvertent omissions were made. These omissions have been corrected herein. Accordingly, additional time is requested so that this Supplemental Information Disclosure Statement can be considered as if properly filed on \_\_\_\_\_.

9. ☐ In accordance with 37 C.F.R. §1.98, a concise explanation of what is presently understood to be the relevance of each non-English language publication is:

*( Check Item 9a, 9b, or 9c)*

- 9a. ☐ satisfied because all non-English language publications were cited on the enclosed English language copy of the PCT International Search Report or the search report from a counterpart foreign application indicating the degree of relevance found by the foreign office.

- 9b. ☐ set forth in the application.


- 9c. ☐ enclosed as an attachment hereto.

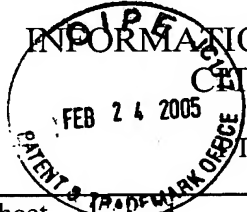
10. ☒ The Commissioner is authorized to charge any additional fee required or credit any overpayment for this Information Disclosure Statement and/or Petition to Morgan, Lewis & Bockius LLP Deposit Account No. 50-0310 (order no. 60809-0127-US).

11. ☒ No admission is made that the information cited in this Statement is, or is considered to be, material to patentability nor a representation that a search has been made (other than a search report of a foreign counterpart application or PCT International Search Report if submitted herewith). 37 C.F.R. §§1.97(g) and (h).

Respectfully submitted,

Date: February 23, 2005

  
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			<b>Complete If Known</b>	
			Application Number	10/662,204
			Filing Date	September 12, 2003
			First Named Inventor	Nguyen et al.
			Art Unit	2819
			Examiner Name	Cho, James Hyonchol
Sheet <u>1</u>	of	2	Attorney Docket Number	60809-0127-US

U.S. PATENT DOCUMENTS						
Examiner Initials	Document Number Number - Kind Code <sup>1</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Class	Subclass	Filing Date if Appropriate
	5,182,467	01/1993	Taylor et al.	327	175	
	5,194,765	03/1993	Dunlop et al.	326	87	
	5,254,883	10/1993	Horowitz et al.	326	30	
	5,373,477	12/1994	Sugibayashi, Tadahiko	327	541	
	5,430,400	07/1995	Herlein	327	108	
	5,530,377	06/1996	Walls	326	30	
	5,783,956	07/1998	Ooishi, Tsukasa	327	143	
	5,821,808	10/1998	Fujima, Shiro	327	538	
	5,847,578	12/1998	Noakes et al.	326	39	
	5,933,051	08/1999	Tsuchida et al.	323	315	
	5,949,254	09/1999	Keeth	326	87	
	5,959,481	09/1999	Donnelly et al.	327	170	
	6,047,346	04/2000	Lau et al.	710	126	
	6,163,178	12/2000	Stark et al.	327	108	
	6,222,354	04/2001	Song, Ki-Whan	323	273	
	6,342,800	01/2002	Stark et al.	327	170	
	6,618,786	09/2003	Sidiropoulos et al.	710	305	
	6,657,468	12/2003	Best et al.	327	170	
	6,661,268	12/2003	Stark et al.	327	170	

FOREIGN PATENT DOCUMENTS							
Examiner Initials	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Class	Subclass	Translation	
	Country Code <sup>2</sup> - Number <sup>3</sup> - Kind Code <sup>4</sup> (if known)					Yes	No
	EP 0 463 316 A1	01/02/1992		H04L	12/40		
	EP 0 482 392 A2	04/29/1992		H04L	25/08		
	JP 58-54412 (A)	03/31/1983		G05F1	56		

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS	
Examiner Initials	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published
	Allen, Arnold O., "Probability, Statistics, and Queuing Theory with Computer Science Applications", 2nd Edition, CH 7, pp 450, 458-459
	Chappell, Terri I. et al., "A 2ns Cycle, 4ns Access 512 kb CMOS ECL SRAM", <i>IEEE International Solid State Circuits Conference</i> , 1991, pp 50-51
	Donnelly, Kevin S. et al., "A 660 MB/s Interface Megacell Portable Circuit in 0.3 $\mu$ m-0.7 $\mu$ m CMOS ASIC", <i>IEEE Journal of Solid State Circuits</i> , 31(12):1995-2003 (1996)

Examiner Signature	Date Considered
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>2</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>3</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>4</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>5</sup> Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

<b>INFORMATION DISCLOSURE CITATION</b>  PTO-1449				<b>Complete If Known</b>	
				<b>Application Number</b>	10/662,204
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				<b>Art Unit</b>	2819
				<b>Examiner Name</b>	Cho, James Hyonchol
<b>Sheet</b>	<b>2</b>	<b>of</b>	<b>2</b>	<b>Attorney Docket Number</b>	60809-0127-US
	Pilo, Harold et al., "A 300 MHz 3.3V 1 Mb SRAM Fabricated in a 0.5 $\mu$ m CMOS Process", <i>IEEE International Solid State Circuits Conference</i> , pp. 148-149 (1996)				
	Schumacher, Hans-Jürgen et al., "CMOS Subnanosecond True-ECL Output Buffer", <i>IEEE Journal of Solid-State Circuits</i> , <b>25</b> (1):150-154 (1990)				
	Sidiropoulos, Stefanos et al., "A 700-Mb/s/pin CMOS Signaling Interface Using Current Integrating Receivers", <i>IEEE Journal of Solid-State Circuits</i> , <b>32</b> (5):681-690 (1997)				
	Yang, Tsen-Shau et al., "A 4-ns 4Kx1-bit Two-Port BiCMOS SRAM", <i>IEEE Journal of D-State Circuits</i> , <b>23</b> (5):1030-1040 (1988)				
	EIA/JDEC, JC-42.3 Committee on RAM Memories, Minutes of Meeting 63, July 21, 1992, Denver, CO.				
	EIA/JDEC, JC-42.3 Committee on RAM Memories, Minutes of Meeting 64, Sept. 16-17, 1992, Crystal City, VA.				

<b>Examiner Signature</b>		<b>Date Considered</b>	
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